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U.S. **UTILITY** Patent Application SCANNED CES

PATENT DATE

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	PLICATION NO. 09/829793	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER		
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Data processing circuit with a cache memory and apparatus containing such a circuit

ORIGIN	CROSS REFERENCE(S)							
CLASS	SUBCLASS	CLASS	CROSS REFERENCE(S) CLASS SUBCLASS (ONE SUBCLASS PER BLOCK)					
VTERNATIONAL	L CLASSIFICATION							
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TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED		
SIGULATIVER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.	
The term of this patent subsequent to (date)				NOTICE OF ALL	OWANCE MAILED	
has been disclaimed.	(Assistant Examiner) (Date)					
The term of this patent shall not extend beyond the expiration date						
of U.S Patent. No			ISSUE FEE			
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